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PTO/SB/08A (07-05)

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Substitute for form 1449/PTO		<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(Use as many sheets as necessary)</i>		Application Number	09/997,530
		Filing Date	11/30/2001
		First Named Inventor	MASTER, Paul L. et al.
		Art Unit	2183
		Examiner Name	Pan, Daniel H.
Sheet 1 of 2	Attorney Docket Number	QuickSilver Technology	

U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (If known)			
h		US- 5,450,557	09/12/1995	Kopp et al.	Col. 1 - 4
h		US- 6,047,115	04/04/2000	Mohan et al.	Col. 3 - 5
h		US- 5,479,055	12/26/1995	Eccles	Col. 1 - 3
h		US- 6,433,578	08/13/2002	Wasson	Col. 6-8
h		US- 5,963,048	10/05/1999	Hartson et al.	Fig. 4
h		US- 4,694,416	09/15/1987	Wheeler et al.	Col. 2- 6
h		US- 6,282,627	08/28/2001	Wong et al.	Col. 2, 4, 5
h		US- 6,353,841	03/05/2002	Marshall et al.	Col. 6, Fig. 3
h		US- 5,910,733	06/08/1999	Bertollet et al.	Col. 5, 7
h		US- 5,886,537	03/23/1999	Macias et al.	general background
h		US- 6,538,470	03/25/2003	Langhammer et al.	Col. 2- 6
h		US- 6,408,039	08/18/2002	Ito	Col. 3
h		US- 5,956,518	09/21/1999	DeHon et al.	Fig. 6
h		US- 6,556,044	04/29/2003	Langhammer et al.	Col. 2- 4
		US-			
		US-			
		US-			
		US-			
		US-			

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T*
		Country Code <sup>2</sup> Number <sup>3</sup> Kind Code <sup>4</sup> (If known)				

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

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**Complete if Known**

Application Number	09/997,530
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First Named Inventor	MASTER, Paul L. et al.
Art Unit	2183
Examiner Name	Pan, Daniel H.
Attorney Docket Number	QuickSilver Technology

Sheet 2

of

2

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
L		ZHANG, HUI et al., "A 1V Heterogeneous Reconfigurable Processor IC for Baseband Wireless Applications", 2000 IEEE Solid-State Circuits Conference, 7 February 2000, pp. 68-69, XP010378803, ISBN: 07803-5853-8/00	
n		ALSOLAIM, A et al., "Architecture and Application of a Dynamically Reconfigurable Hardware Array for Future Mobile Communication Systems", Field Programmable Custom Computing Machines, 2000 IEEE Symposium, Napa Valley, Los Alamitos, CA 17-19 April 2000, IEEE Comput. Soc., 17 April 2000, pp. 205-214, XP010531939, ISBN 0-7695-0871-5/00	
n		BECKER, J et al., "Design and Implementation of a Coarse-Grained Dynamically Reconfigurable Hardware Architecture", VLSI 2001, Proceedings IEEE Computer Soc. Workshop, 19-20 April 2001, Piscataway, NJ, USA, 19 April 2001, pp. 41-46, XP010541755, ISBN: 0-7695-1056-8/01	
n		ABNOUS A. et al., "Ultra-low-power domain-specific multimedia processors", VLSI SIGNAL PROCESSING, IX, 1998, IEEE WORKSHOP IN SAN FRANCISCO, CA, USA, 30 Oct - 1 Nov. 1998, 30 October 1998, pp. 461-470, XP010199037, ISBN: 0-7803-3134-8.	
n		BAUMGARTE, V. et al., "PACT XPP - A Self-Reconfigurable Data Processing Architecture", NN, 25 June 2001, XP002256066	
n		HARTENSTEIN, R. "Coarse Grain Reconfigurable Architectures", Design Automation Conference, 2001. Proceedings of the ASP-DAC 2001, Asia and South Pacific 30 Jan-2 Feb 2001, Piscataway, NJ, US, IEEE 30 Jan 2001, pp. 564-569, XP010537887, ISBN: 0-7803-6833-8	

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